

UNLESS NOTED:
RESISTORS: OHMS, 0402, 1%
CAPACITORS: 0603

REVISION HISTORY

ECO	REV	DESCRIPTION	APPROVED	DATE
-	3	PRODUCTION	JIM N.	05 - 23 - 16

*** VOUT JUMPER CONFIGURATION**

JP2 / VS1	JP3 / VS2	LTC3130-1 VOUT
GND	GND	1.8V
VCC	GND	3.3V
GND	VCC	5.0V
VCC	VCC	12V

NOTES:
1) Connect GND of R3, R6, C11, C12 and C13 directly to pins 7 and 8. Connect pins 7 and 8 to IC Pad directly under IC. Connect pins 18 and 19 to copper plane and IC pad directly under IC.

CUSTOMER NOTICE
LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE.

APPROVALS

PCB DES.	NC
APP ENG.	J.NOON

THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.

SCALE = NONE

DATE: 05-23-16

LINEAR TECHNOLOGY
1630 McCarthy Blvd.
Milpitas, CA 95035
Phone: (408)432-1900 www.linear.com
Fax: (408)434-0507
LTC Confidential-For Customer Use Only

TITLE: SCHEMATIC 25V, 600mA BUCK-BOOST DC / DC CONVERTER WITH 1.6uA QUIESCENT CURRENT

SIZE N/A

IC NO. LTC3130UDC-1

DEMO CIRCUIT 2397A

REV. 3

SHEET 1 OF 1

UNLESS NOTED:
RESISTORS: OHMS, 0402, 1%
CAPACITORS: 0603

JP2 / VS1	JP3 / VS2	LTC3130-1 VOUT
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VCC	GND	3.3V
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VCC	VCC	12V

REVISION HISTORY

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-	3	PRODUCTION	JIM N.	05 - 23 - 16

NOTE: FOR MPPC, REMOVE R4 AND POPULATE R5 AND R6. DO NOT ALLOW MPPC PIN VOLTAGE TO EXCEED 6.0V.

NOTES:

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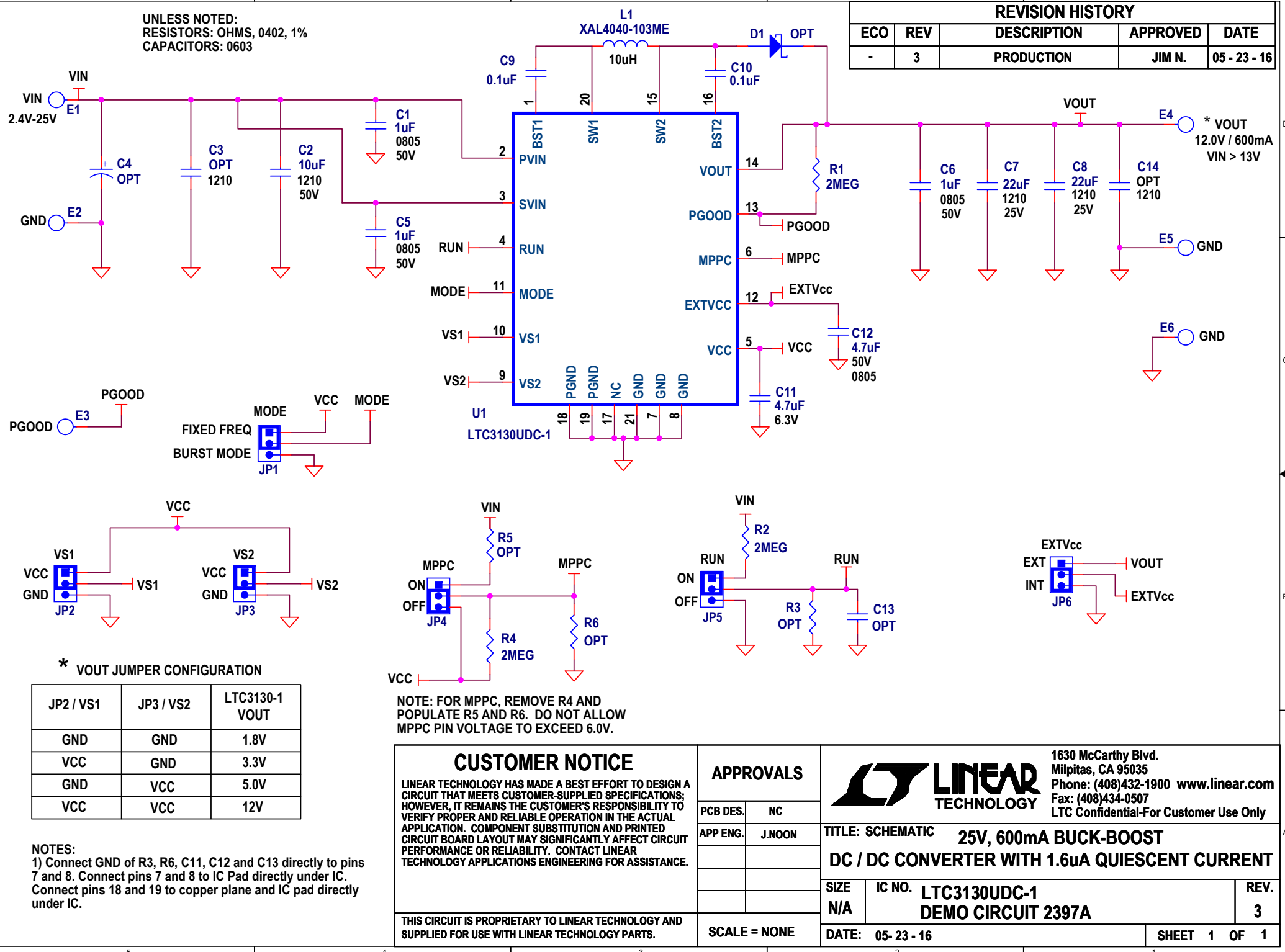
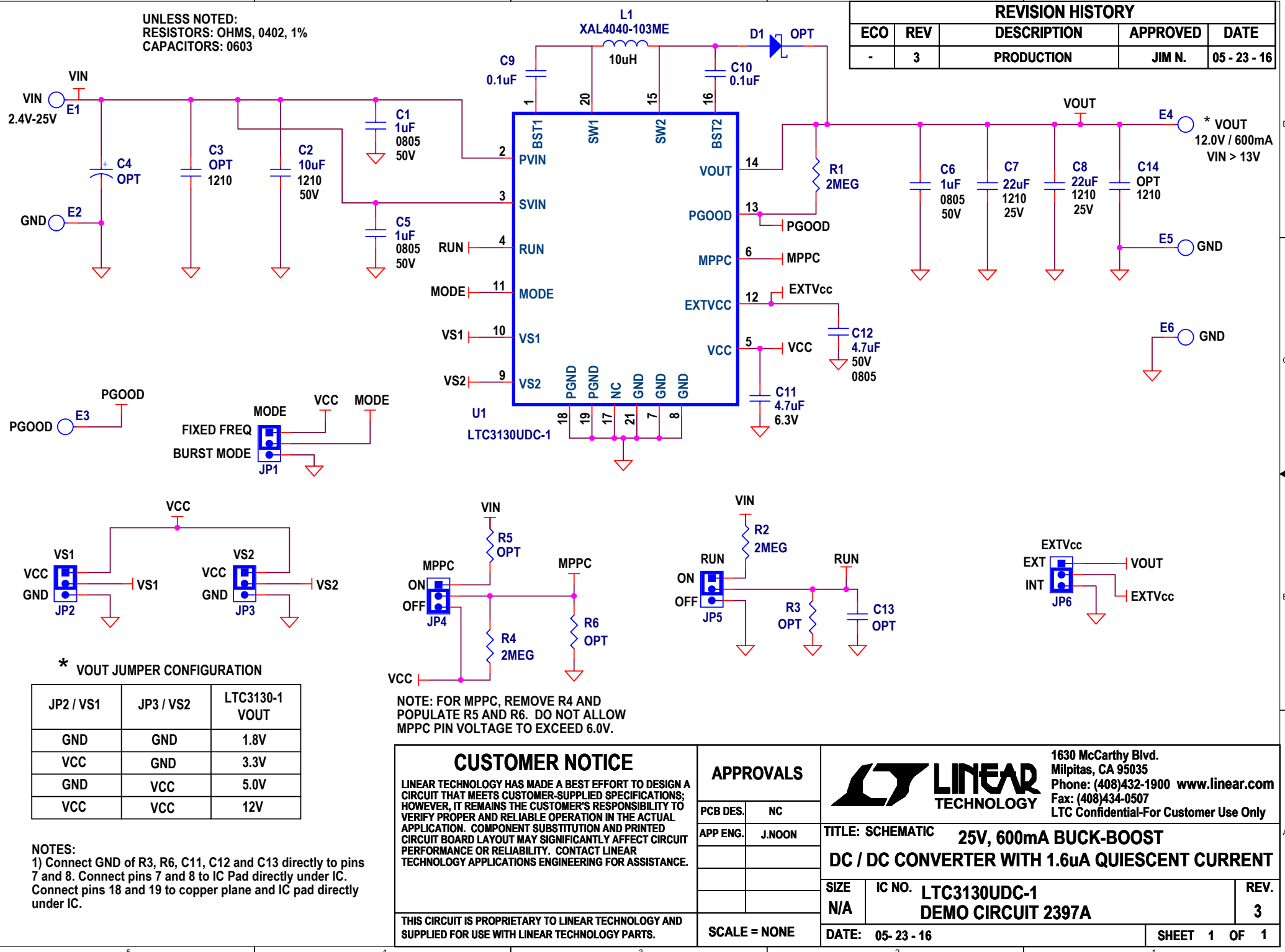
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APP ENG.	J.NOON

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TITLE: SCHEMATIC		25V, 600mA BUCK-BOOST DC / DC CONVERTER WITH 1.6uA QUIESCENT CURRENT	
SIZE	IC NO.		
N/A	LTC3130UDC-1 DEMO CIRCUIT 2397A	REV. 3	
DATE: 05-23-16		SHEET 1 OF 1	



**UNLESS NOTED:
RESISTORS: OHMS, 0402, 1%
CAPACITORS: 0603**

REVISION HISTORY

ECO	REV	DESCRIPTION	APPROVED	DATE
-	3	PRODUCTION	JIM N.	05 - 23 - 16

* VOUT
12.0V / 600mA
VIN > 13V

*** VOUT JUMPER CONFIGURATION**

JP2 / VS1	JP3 / VS2	LTC3130-1 VOUT
GND	GND	1.8V
VCC	GND	3.3V
GND	VCC	5.0V
VCC	VCC	12V

NOTES:
1) Connect GND of R3, R6, C11, C12 and C13 directly to pins 7 and 8. Connect pins 7 and 8 to IC Pad directly under IC. Connect pins 18 and 19 to copper plane and IC pad directly under IC.

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SCALE = NONE

DATE: 05-23-16

SHEET 1 OF 1

UNLESS NOTED:
RESISTORS: OHMS, 0402, 1%
CAPACITORS: 0603

REVISION HISTORY

ECO	REV	DESCRIPTION	APPROVED	DATE
-	3	PRODUCTION	JIM N.	05 - 23 - 16

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DATE: 05-23-16

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**TITLE: SCHEMATIC 25V, 600mA BUCK-BOOST
DC / DC CONVERTER WITH 1.6uA QUIESCENT CURRENT**

**SIZE N/A IC NO. LTC3130UDC-1
DEMO CIRCUIT 2397A**

REV. 3

SHEET 1 OF 1

UNLESS NOTED:
RESISTORS: OHMS, 0402, 1%
CAPACITORS: 0603

REVISION HISTORY

ECO	REV	DESCRIPTION	APPROVED	DATE
-	3	PRODUCTION	JIM N.	05 - 23 - 16

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APP ENG.	J.NOON

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SIZE	IC NO.	REV.
N/A	LTC3130UDC-1 DEMO CIRCUIT 2397A	3

DATE: 05- 23 - 16

SHEET 1 OF 1

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REVISION HISTORY

ECO	REV	DESCRIPTION	APPROVED	DATE
-	3	PRODUCTION	JIM N.	05 - 23 - 16

*** VOUT JUMPER CONFIGURATION**

JP2 / VS1	JP3 / VS2	LTC3130-1 VOUT
GND	GND	1.8V
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GND	VCC	5.0V
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APP ENG.	J.NOON

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SIZE	IC NO.	REV.
N/A	LTC3130UDC-1 DEMO CIRCUIT 2397A	3

DATE: 05- 23 - 16

SHEET 1 OF 1

Linear Technology Corporation

LTC3130UDC-1

ENGR: J. Noon (011-086)

25V, 600mA Buck Boost DC/DC Converter

BILL OF MATERIALS

DC2397A-3

6/10/2016

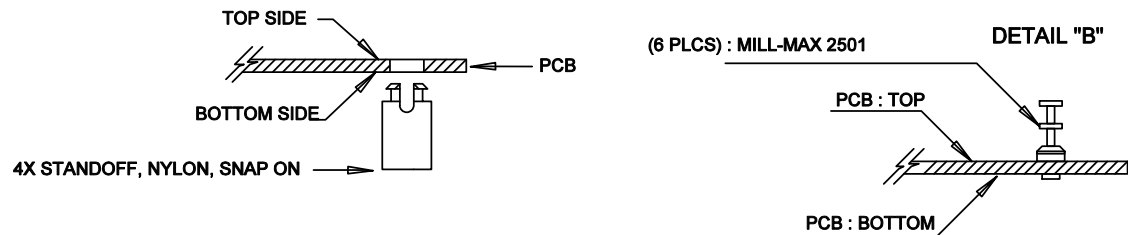
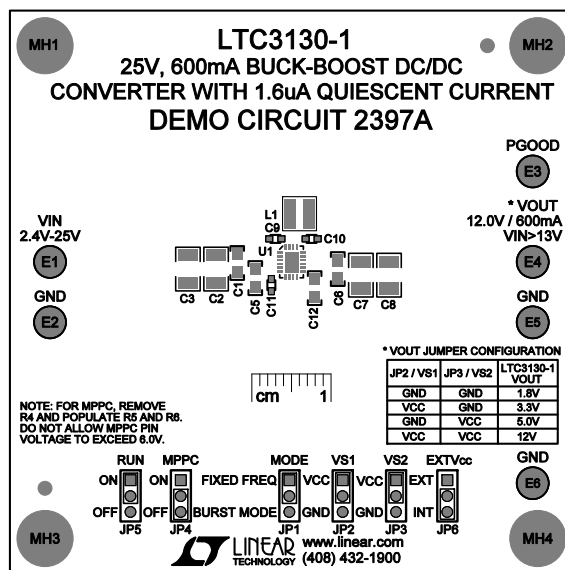
Item	Qty	Reference	Part Description	Manufacture / Part #	Kit Qty
				NUMBER OF BOARDS =	325
1	3	C1, C5, C6	CAP CER 1UF 50V 10% X7R 0805	MURATA, GRM21BR71H105KA12L	975
2	1	C2	CAP CER 10UF 50V X7R 1210	MURATA, GRM32ER71H106KA12L	325
3	0	C3, C14	CAP, 1210 (OPT)		0
4	0	C4	CAP ALUM 220UF 35V 20% SMD (OPT)	PANASONIC, EEE-FP1V221AP	0
5	2	C7, C8	CAP CER 22UF 25V X7R 1210	MURATA, GRM32ER71E226KE15L	650
6	2	C9, C10	CAP CER 0.1UF 50V X7R 0603	MURATA, GRM188R71H104KA93D	650
7	1	C11	CAP CER 4.7UF 6.3V 10% X5R 0603	MURATA, GRM188R60J475KE19D	325
8	1	C12	CAP CER 4.7UF 50V 10% X5R 0805	MURATA, GRM21BR61H475KE51L	325
9	0	C13	CAP, 0603 (OPT)		0
10	3	R1, R2, R4	RES 2M OHM 1/16W 1% 0402 SMD	VISHAY, CRCW04022M00FKED	975
12	0	R3, R5, R6	RES, 0402 (OPT)		0
13	0	D1	DIODE SCHOTTKY 40V 2A SOD123 (OPT)	ROHM, RB068M-40TR	0
14	1	L1	INDUCTOR, 10μH, ±20%	COILCRAFT, XAL4040-103ME	325
15	1	U1	25V, 600mA Buck Boost DC/DC Converter	LINEAR TECHNOLOGY, LTC3130UDC-1#PBF	325
16	6	E1 - E6	TP, TURRET, 0.094", PBF	MILL-MAX, 2501-2-00-80-00-00-07-0	1950
17	6	JP1 - JP6	JMP, 3PIN 1 ROW .079CC	SULLINS, NRPN031PAEN-RC	1950
18	6	XJP1 - XJP6	SHUNT, .079" CENTER	SAMTEC, 2SN-BK-G	1950
19	4		SPACER STACKING #4 SCREW NYLON .500"	KEYSTONE, 8833	1300


Item	Qty	Reference	Part Description	Manufacturer / Part #
		REQUIRED CIRCUIT COMPONENTS:		
1	3	C1, C5, C6	CAP CER 1UF 50V 10% X7R 0805	MURATA, GRM21BR71H105KA12L
2	1	C2	CAP CER 10UF 50V X7R 1210	MURATA, GRM32ER71H106KA12L
3	2	C7, C8	CAP CER 22UF 25V X7R 1210	MURATA, GRM32ER71E226KE15L
4	2	C9, C10	CAP CER 0.1UF 50V X7R 0603	MURATA, GRM188R71H104KA93D
5	1	C11	CAP CER 4.7UF 6.3V 10% X5R 0603	MURATA, GRM188R60J475KE19D
6	1	C12	CAP CER 4.7UF 50V 10% X5R 0805	MURATA, GRM21BR61H475KE51L
7	3	R1, R2, R4	RES 2M OHM 1/16W 1% 0402 SMD	VISHAY, CRCW04022M00FKED
8	1	L1	INDUCTOR, 10μH, ±20%	COILCRAFT, XAL4040-103ME
9	1	U1	25V, 600mA Buck Boost DC/DC Converter	LINEAR TECHNOLOGY, LTC3130UDC-1 #PBF
		ADDITIONAL DEMO BOARD CIRCUIT COMPONENTS:		
10	0	C3, C14	CAP, 1210 (OPT)	
11	0	C4	CAP ALUM 220UF 35V 20% SMD (OPT)	PANASONIC, EEE-FP1V221AP
12	0	C13	CAP, 0603 (OPT)	
13	0	R3, R5, R6	RES, 0402 (OPT)	
14	0	D1	DIODE SCHOTTKY 40V 2A SOD123 (OPT)	ROHM, RB068M-40TR
15	6	E1 - E6	TP, TURRET, 0.094", PBF	MILL-MAX, 2501-2-00-80-00-00-07-0
16	6	JP1 - JP6	SULLINS, NRPN031PAEN-RC	SULLINS, NRPN031PAEN-RC
17	6	XJP1 - XJP6	SHUNT, .079" CENTER	SAMTEC, 2SN-BK-G
18	4		SPACER STACKING #4 SCREW NYLON .500"	KEYSTONE, 8833

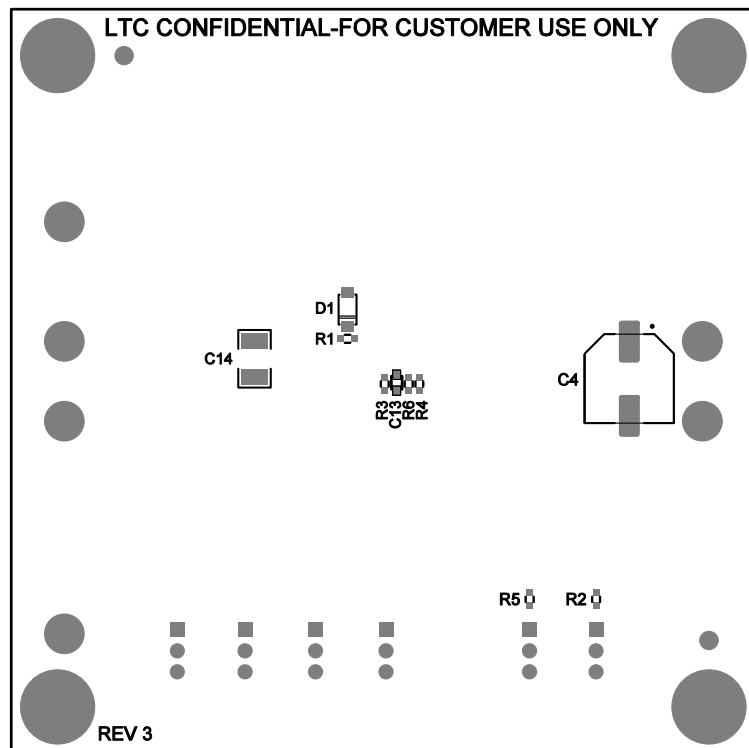
REVISION HISTORY				
ECO	REV	DESCRIPTION	APP. ENG.	DATE
-	3	PRODUCTION	JIM N.	05-23-16


NOTES: UNLESS OTHERWISE SPECIFIED

1. WORKMANSHIP SHALL BE IN ACCORDANCE WITH IPC-A-610.
2. ASSEMBLY REFLOW PROFILE SHALL BE IN ACCORDANCE WITH J-STD-020 WITH MAXIMUM SOLDER TEMPERATURE OF 250 DEGREES CELSIUS.
3. PARTS TO OMIT WILL BE SPECIFIED ON THE BILL OF MATERIALS
LOCATIONS OF OMITTED PARTS SHALL BE FREE OF SOLDER.
MASK THE SOLDER STENCIL WHERE SMT PARTS ARE OMITTED.
4. INSTALL SHUNTS AS SHOWN ON ASSY DRAWING.
5. DEPANELIZE BOARDS AFTER ASSEMBLY AND ROUTE-OUT THE BREAKOUT TABS ON FOUR SIDES OF THE BOARD EDGE.
6. APPLY ASSEMBLY STAMP OR QA STAMP TO BOTTOM OF BOARD (UNSHOWY AREA).
7. INSTALL TURRETS, STAND-OFFS AS SHOWN BELOW:



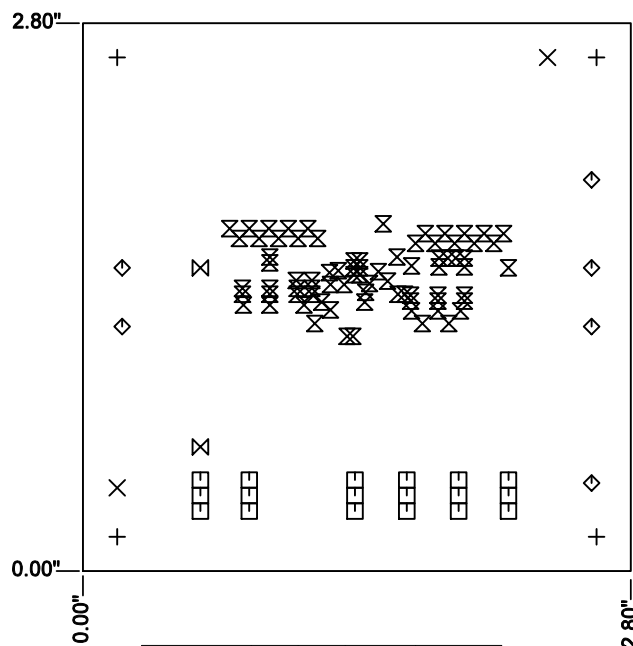
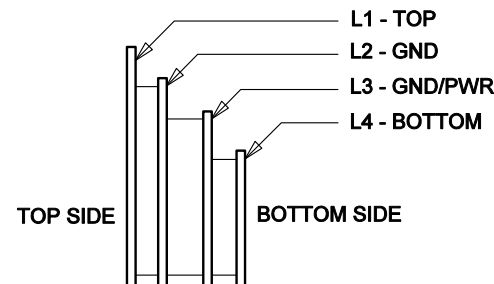
APPROVALS		 1630 MCCARTHY BLVD MILPITAS, CA 95035 PH: (408)432-1900 www.linear.com LTC CONFIDENTIAL- FOR CUSTOMER USE ONLY	
PCB DES	NC		
APP ENG	JIM N.	TITLE: TOP ASSEMBLY DRAWING 25V, 600mA BUCK-BOOST DC/DC CONVERTER WITH 1.6uA QUIESCENT CURRENT	
		SIZE	REV.
		N/A	3
SCALE = NONE		SHT 1 OF 1	



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PCB DES.	NC			
APP ENG.	JIM N.	TITLE: BOTTOM ASSEMBLY DRAWING 25V, 600mA BUCK-BOOST DC/DC CONVERTER WITH 1.6uA QUIESCENT CURRENT		
		SIZE	IC NO. LTC3130-1	REV
		N/A	DEMO CIRCUIT 2397A	3
SCALE = NONE				SHT 2 of 2

REVISION HISTORY				
ECO	REV	DESCRIPTION	APP. ENG.	DATE
-	3	PRODUCTION	JIM N.	05-23-16

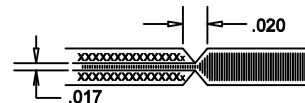
LAYER STRUCTURE

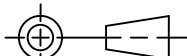



SIZE	QTY	SYM	PLATED	TOL
0.19	4	+	YES	+/-0.003"
0.07	2	X	NO	+/-0.003"
0.035	18	□	YES	+/-0.003"
0.094	6	◇	YES	+/-0.003"
0.01	78	X	YES	+/-0.003"
0.015	2	X	YES	+/-0.003"

NOTES: UNLESS OTHERWISE SPECIFIED

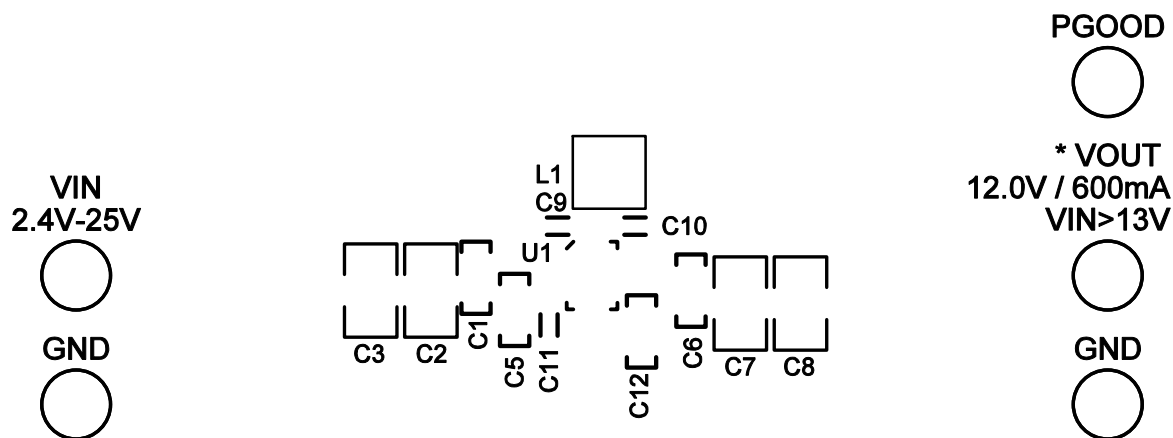
- FAB PER IPC-A-600, CLASS 2.
- MATERIAL:
 - EPOXY FIBERGLASS, NEMA GRADE FR-4
 - FINISHED THICKNESS TO BE 0.062" +/- .005"
 - TOTAL OF 4 LAYERS WITH 2 OZ. CU ON THE OUTER LAYERS AND 1 OZ. CU ON THE INNER LAYERS.
 - FLAMMABILITY RATING: 94 V-0 MINIMUM.
- SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN 0.00" ARE PRIMARY DATUMS.
- DRILLING:
 - DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.001" THICK MIN.
 - ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
 - HOLE LOCATION TOLERANCES ARE +/-0.003" IN RELATION TO CENTER.
- FINISH:
 - SMOBC USING LPI BOTH SIDES, COLOR GREEN. TENT BOTH SIDES.
 - GOLD IMMERSION BOTH SIDES.
 - FOR SILKSCREEN: BOTH SIDES USE WHITE NON-CONDUCTIVE INK.
- DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE. PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
- PCBS ARE TO BE RoHS COMPLIANT.
- SCORING FOR PANELIZED PCB:



<div>UNLESS OTHERWISE SPECIFIED</div> <div>DIMENSIONS ARE IN INCHES</div> <div>TOLERANCES:</div> <div>0.XX" = 0.01"</div> <div>0.XXX" = 0.005"</div> <div>INTERPRET DIM AND TOL</div> <div>PER ASME Y14.5M-1994</div> <div>THIRD ANGLE PROJECTION</div> <div></div>	<div>APPROVALS</div>		<div> LINEAR TECHNOLOGY</div> <div>1630 MCCARTHY BLVD MILPITAS, CA 95035 PH: (408)432-1900 www.linear.com LTC CONFIDENTIAL- FOR CUSTOMER USE ONLY</div>	
	PCB DES	NC		
	APP ENG	JIM N.	<div>TITLE: FABRICATION DRAWING</div> <div>25V, 600mA BUCK-BOOST DC/DC CONVERTER WITH 1.6uA QUIESCENT CURRENT</div>	
		SIZE	IC NO. LTC3130-1	REV
		N/A	DEMO CIRCUIT 2397A	3
	SCALE = NONE			SHT 1 OF 1

LTC3130-1

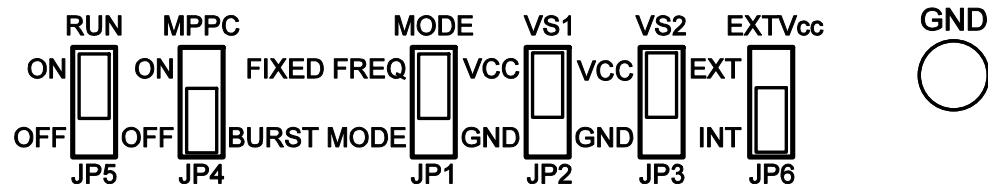
25V, 600mA BUCK-BOOST DC/DC CONVERTER WITH 1.6uA QUIESCENT CURRENT DEMO CIRCUIT 2397A



NOTE: FOR MPPC, REMOVE
R4 AND POPULATE R5 AND R6.
DO NOT ALLOW MPPC PIN
VOLTAGE TO EXCEED 6.0V.

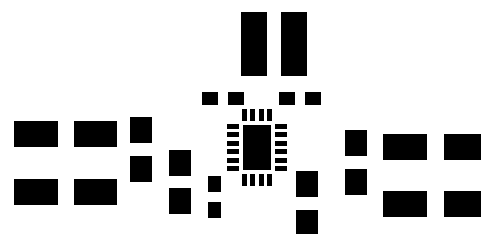
* VOUT JUMPER CONFIGURATION

JP2 / VS1	JP3 / VS2	LTC3130-1 VOUT
GND	GND	1.8V
VCC	GND	3.3V
GND	VCC	5.0V
VCC	VCC	12V

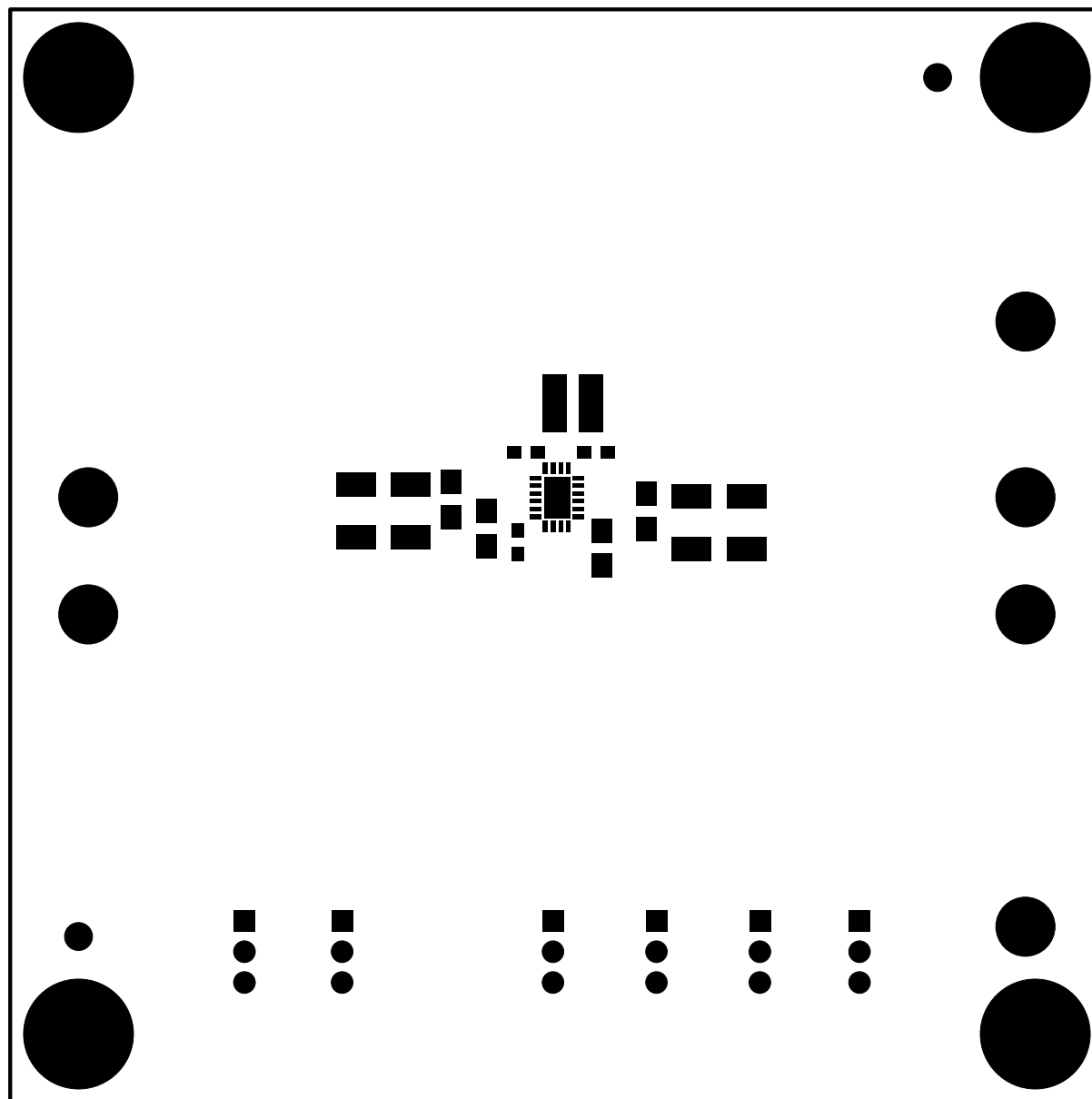


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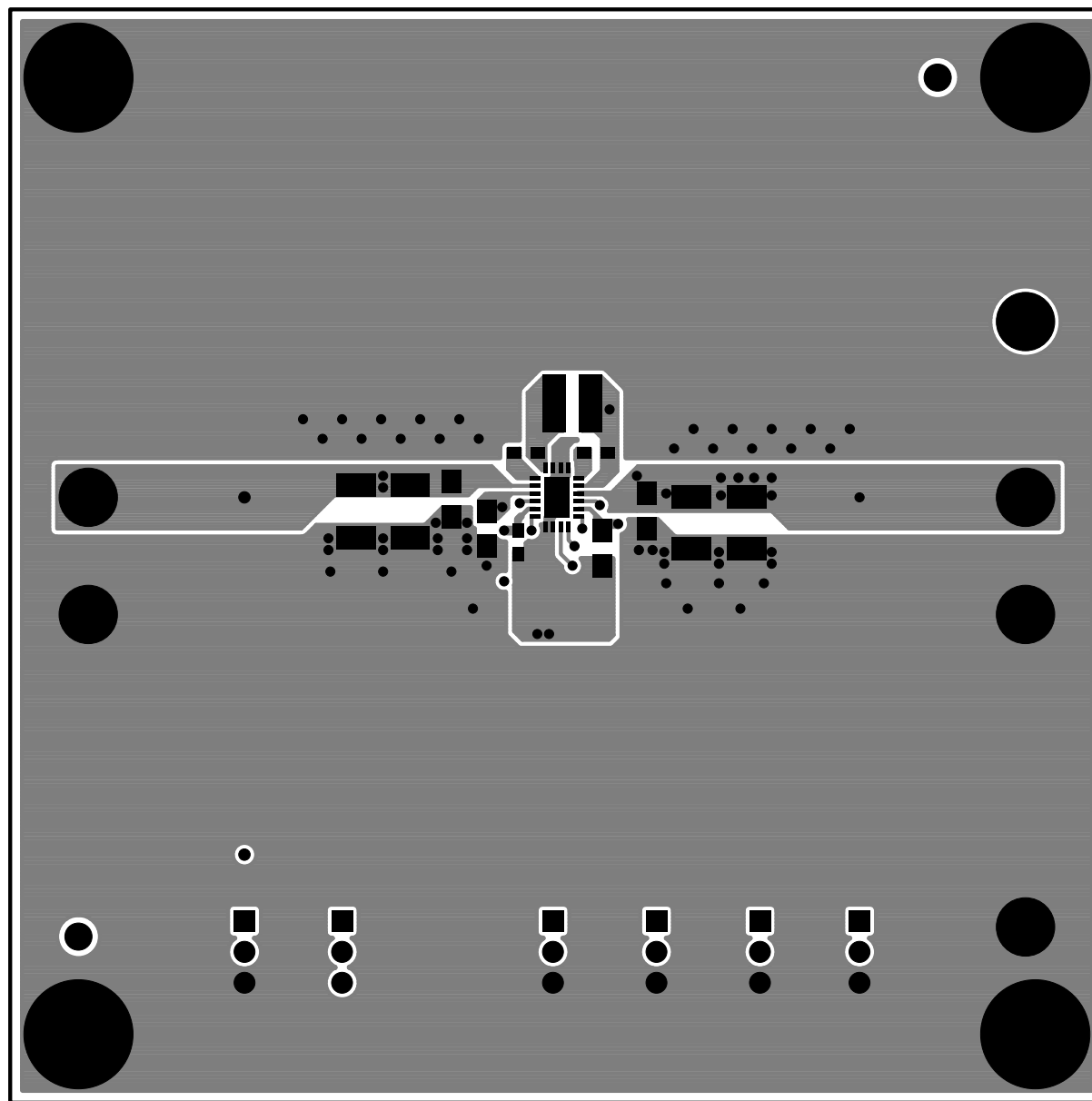
TOP SILKSCREEN
LINEAR TECH CORP.
DEMO CIRCUIT 2397A-3 * LTC3130-1
DATE: 05-23-16



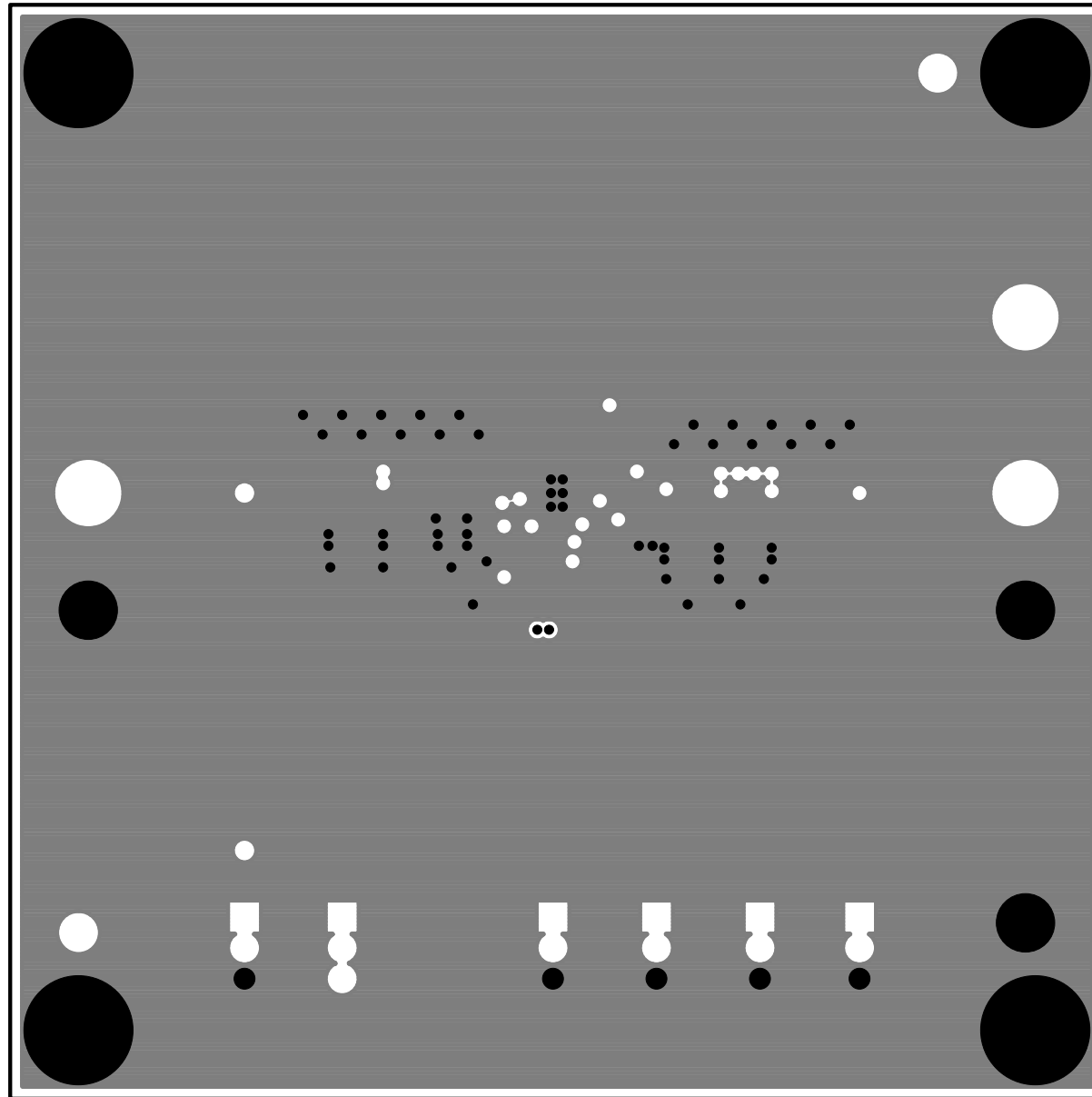
TOP SOLDER PASTE
LINEAR TECH CORP.
DEMO CIRCUIT 2397A-3 * LTC3130-1
DATE: 05-23-16



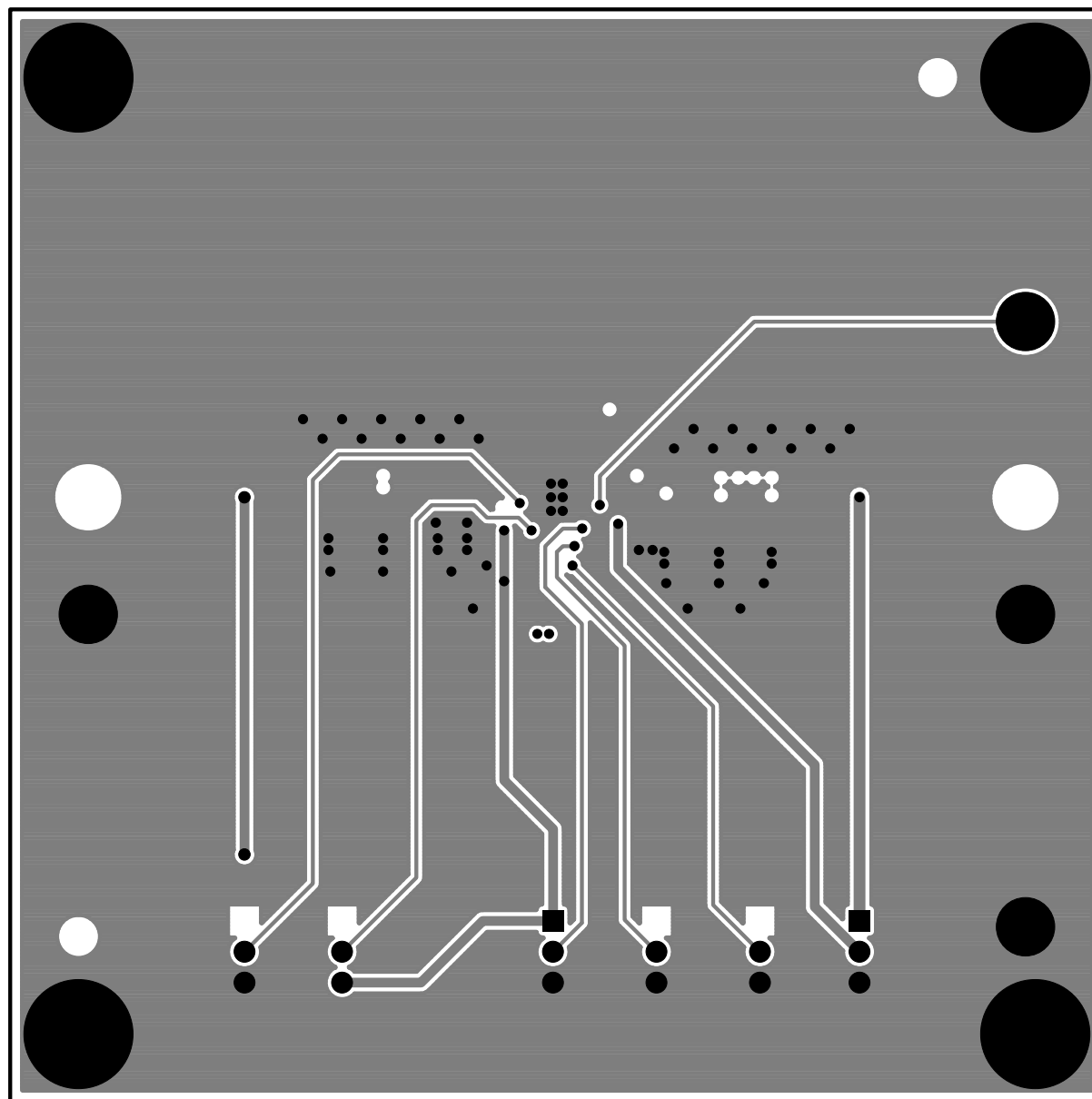
TOP SOLDER MASK
LINEAR TECH CORP.
DEMO CIRCUIT 2397A-3 * LTC3130-1
DATE: 05-23-16



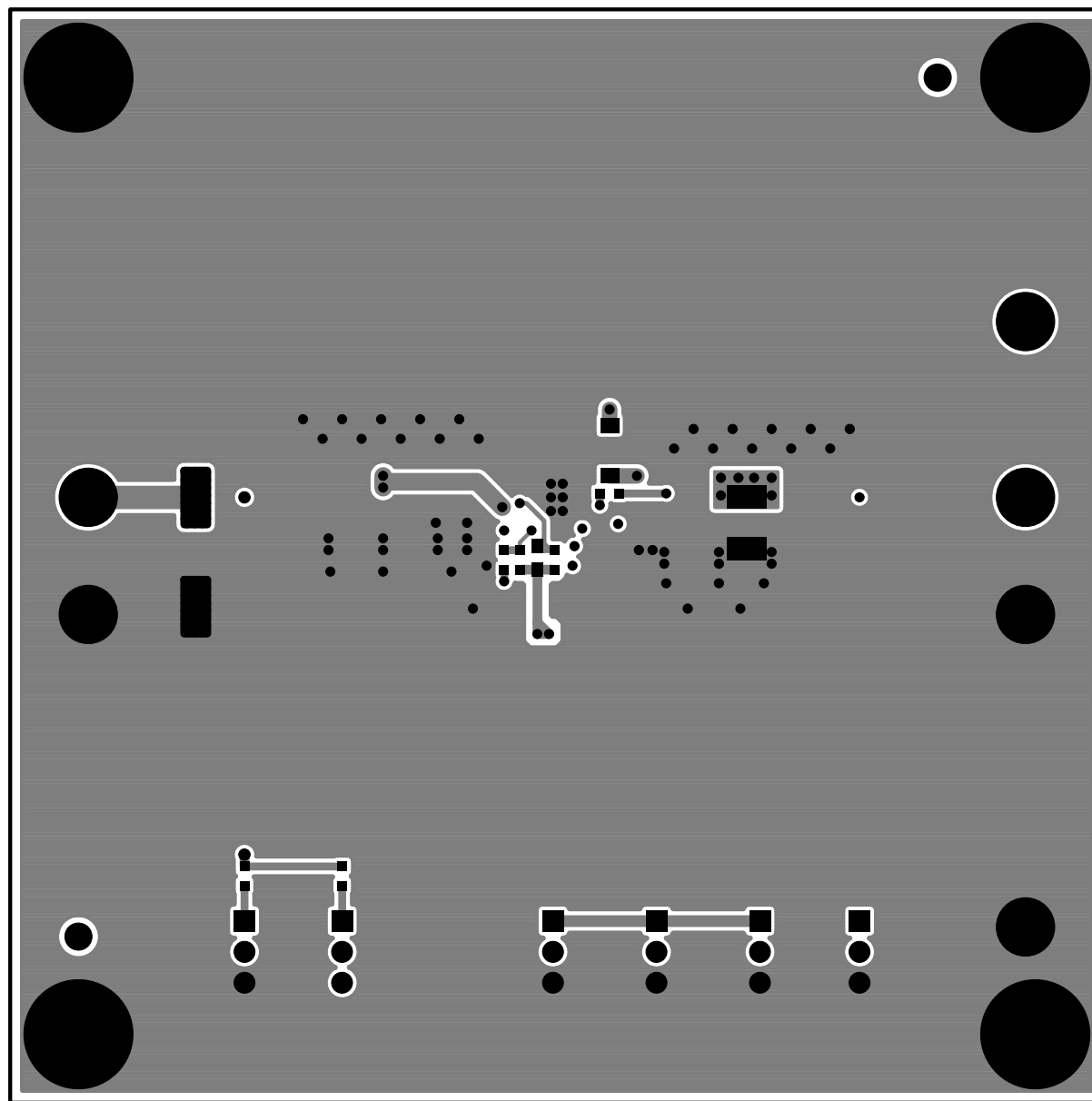
LAYER 1 : TOP LAYER
LINEAR TECH CORP.
DEMO CIRCUIT 2397A-3 * LTC3130-1
DATE: 05-23-16



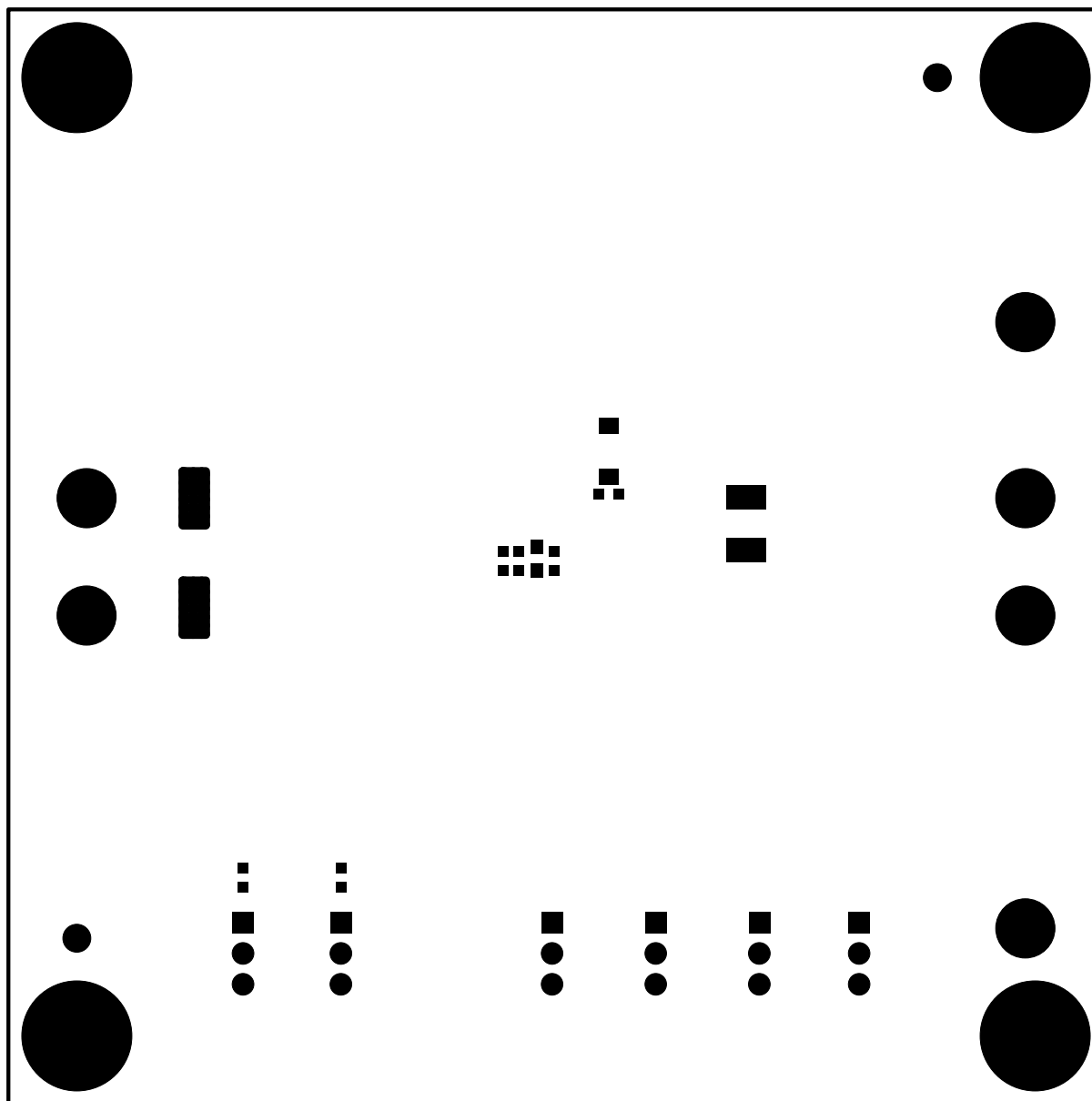
LAYER 2 : - GND 1
LINEAR TECH CORP.
DEMO CIRCUIT 2397A-3 * LTC3130-1
DATE: 05-23-16



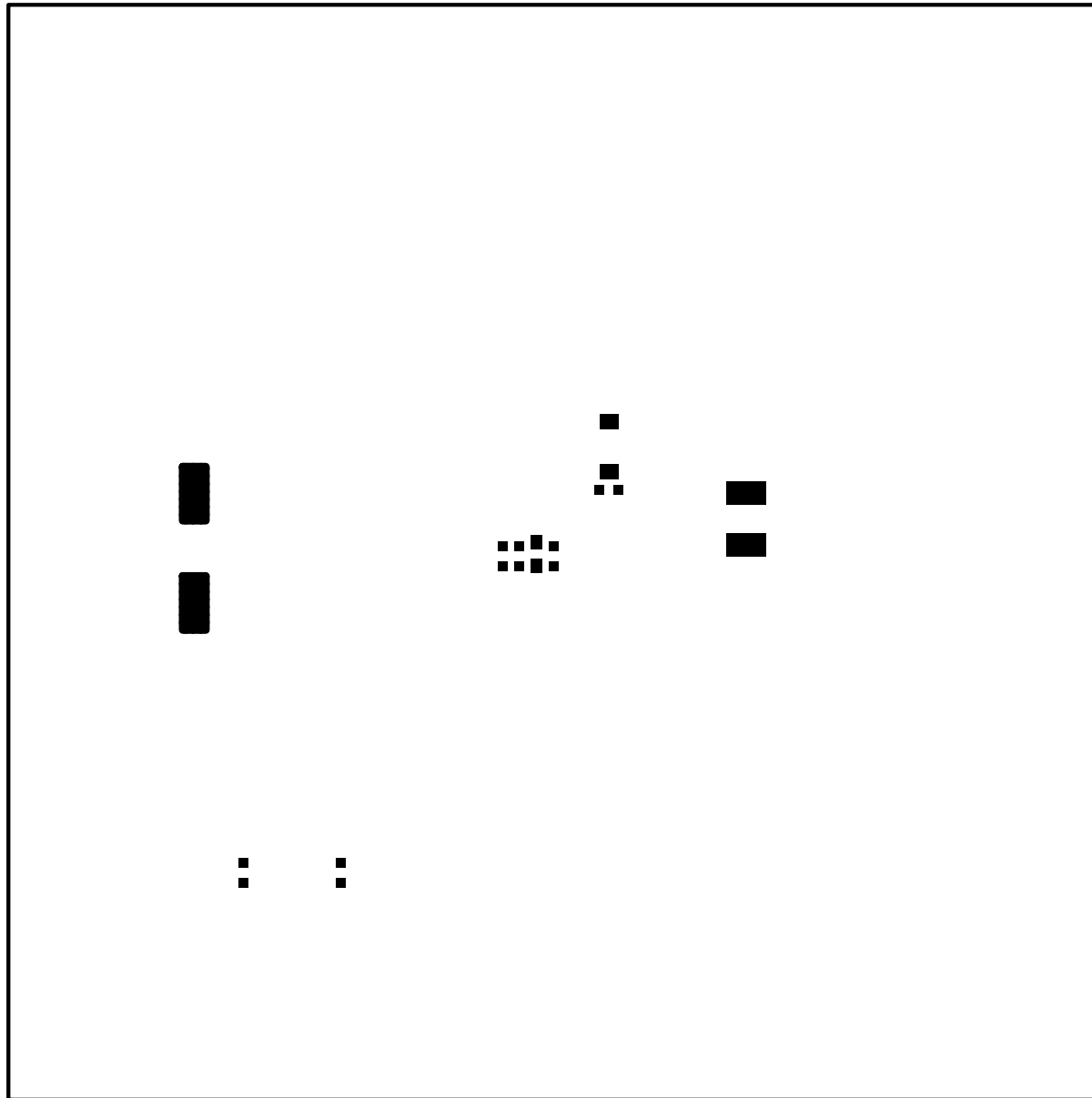
LAYER 3 : - GND 2
LINEAR TECH CORP.
DEMO CIRCUIT 2397A-3 * LTC3130-1
DATE: 05-23-16



LAYER 4 : BOTTOM LAYER
LINEAR TECH CORP.
DEMO CIRCUIT 2397A-3 * LTC3130-1
DATE: 05-23-16

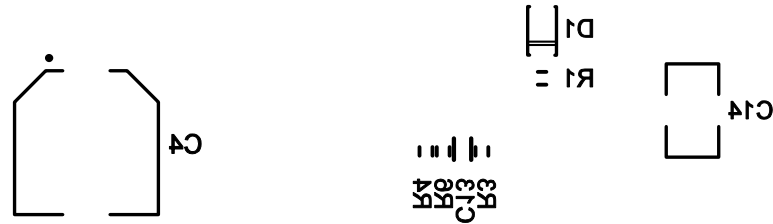


BOTTOM SOLDER MASK
LINEAR TECH CORP.
DEMO CIRCUIT 2397A-3 * LTC3130-1
DATE: 05-23-16



BOTTOM SOLDER PASTE
LINEAR TECH CORP.
DEMO CIRCUIT 2397A-3 * LTC3130-1
DATE: 05-23-16

LTC CONFIDENTIAL-FOR CUSTOMER USE ONLY



REV 3

BOTTOM SILKSCREEN
LINEAR TECH CORP.
DEMO CIRCUIT 2397A-3 * LTC3130-1
DATE: 05-23-16